Citizenship: USA

Post Office Address: 608 Boise Hills Drive, Boise, ID 83702

ATION FOR PATENT APPLICATION (WITH POWE

ver ATTORNEY)

As an inventor named below or on any attached continuation page, I hereby declare that:

My residence, post office address and citizenship are as stated next to my name.

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled METHOD FOR IN-LINE TESTING OF FLIP-CHIP SEMICONDUCTOR ASSEMBLIES, the specification of which (check one):

	ted States application serial no and was		•	
D was filed onas PC	international application no and was a	amended under PCT Article 19 on	·	
·	I understand the contents of the above-identified spe	cification, including the claims, as amend	ed by any a	mendme
referred to above.				
	he U.S. Patent and Trademark Office all information		ntability of th	he subje
matter claimed in this application, as "ma	teriality" is defined in Title 37, Code of Federal Rep	gulations § 1.56.		
I hereby claim foreign priority benefit	under Title 35, United States Code, § 119(a)-(d) o	r § 365(b) of any foreign application(s) fo	or patent or	inventor
	tional application(s) designating at least one country			
	dentified below and on any attached continuation pay nating at least one country other than the United Stat			
application(s) on which priority is claimed	•			
Prior foreign/PCT application(s):				
not loteign of apparation(s).			Priority C	Claimed
(number)	(country)	(day/month/year filed)	Yes	No
	C. June 1	(
(number)	(country)	(day/month/year filed)	Yes	No
I hereby claim the benefit under Title 3	5, United States Code, § 120 of any United States a	pplication(s) or 8 365(c) of PCT internati	ional annlica	uion(e)
	sted below and on any attached continuation page an			
	r application in the manner provided by the first pa			
	Frademark Office all information known to me to be			
egulations 8 1.56 which became available	between the filing date of such prior application and	d the national or PCT international filing	date of this	onnlice
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(application serial no.)	(filing date)	(status - pending, patented or ab	andoned)	·
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(application serial no.)	(filing date)	(status - pending, patented or ab	andoned)	
I hereby claim the ber	efit under Title 35, United States Code, § 119(e) of	any United States provisional application	n(s) listed be	low:
(provisional application no.)	(filing date)			
(applied adjection to)	(Slice des)	·		
(provisional application no.)	(filing date)			
(provisional application no.)				
(provisional application no.)	(filing date)			
-	(filing date)			
I hereby appoint the following Registered	(filing date) Practitioners to prosecute this application and to tra	ansact all business in the Patent and Trad	emark Office	B
I hereby appoint the following Registered therewith:	Practitioners to prosecute this application and to tra		emark Office	В
I hereby appoint the following Registered therewith: David V. Trask, Reg. No. 22,012	Practitioners to prosecute this application and to tra- William S. Britt, Reg. No. 20,969	Thomas J. Rossa, Reg. No. 26,799	emark Office	
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DECLARATION FOR PATENT APPLICATION

(continuation page)

Invention title: METHOD FOR IN-LINE TESTING OF FLIP-CHIP SEMICONDUCTOR ASSEMBLIES

Inventor name(s) appearing on first declaration page: Chad A. Cobbley

Additional original, first and joint inventor(s):

Full name of second joint inventor: John VanNortwick Inventor's signature
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Full name of third joint inventor: Bret K. Street
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Full name of fourth joint inventor: Tongbi Jiang

Inventor's signature

Residence: Boise, Idaho

Date 9130/98

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Cobbley et al.

Serial No.: 09/819,472

Filed: March 28, 2001

For: METHOD FOR IN-LINE TESTING OF FLIP-CHIP SEMICONDUCTOR

ASSEMBLIES

Confirmation No.: 3718

Examiner: R. Kobert

Group Art Unit: 2829

Attorney Docket No.: 2269-3437.2US

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

January 8, 2003

Date

Amanda Holland

Name (Type/Print)

ASSOCIATE POWER OF ATTORNEY

Commissioner for Patents Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. § 1.34(b), please recognize the following individual as an associate agent/attorney herein in connection with the above-identified patent application, and with all continuing and divisional applications hereof:

Kevin K. Johanson, Reg. No. 38,506

Respectfully submitted,

Joseph A. Walkowski Registration No. 28,765 Attorney for Applicant(s)

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Date: January 8, 2003

JAW/ah

Document in ProLaw